

Contents

| | |
|--|-------------|
| Preface | xiii |
| Acknowledgments | xvii |
| 1 Introduction | 1 |
| 1.1 Concurrent Programs | 2 |
| 1.2 The Modeling Approach | 4 |
| 1.3 Practice | 7 |
| 1.4 Content Overview | 8 |
| Summary | 9 |
| Notes and Further Reading | 9 |
| 2 Processes and Threads | 11 |
| 2.1 Modeling Processes | 11 |
| 2.2 Implementing Processes | 23 |
| Summary | 31 |
| Notes and Further Reading | 31 |
| Exercises | 32 |
| 3 Concurrent Execution | 37 |
| 3.1 Modeling Concurrency | 38 |
| 3.2 Multi-Threaded Programs | 52 |
| Summary | 59 |
| Notes and Further Reading | 59 |
| Exercises | 60 |
| 4 Shared Objects and Mutual Exclusion | 63 |
| 4.1 Interference | 63 |
| 4.2 Mutual Exclusion in Java | 71 |
| 4.3 Modeling Mutual Exclusion | 73 |

| | |
|---|------------|
| Summary | 77 |
| Notes and Further Reading | 77 |
| Exercises | 78 |
| 5 Monitors and Condition Synchronization | 79 |
| 5.1 Condition Synchronization | 79 |
| 5.2 Semaphores | 86 |
| 5.3 Bounded Buffers | 93 |
| 5.4 Nested Monitors | 97 |
| 5.5 Monitor Invariants | 101 |
| Summary | 102 |
| Notes and Further Reading | 103 |
| Exercises | 104 |
| 6 Deadlock | 107 |
| 6.1 Deadlock Analysis | 108 |
| 6.2 Dining Philosophers Problem | 112 |
| Summary | 119 |
| Notes and Further Reading | 120 |
| Exercises | 120 |
| 7 Safety and Liveness Properties | 123 |
| 7.1 Safety | 124 |
| 7.2 Single-Lane Bridge Problem | 127 |
| 7.3 Liveness | 135 |
| 7.4 Liveness of the Single-Lane Bridge | 140 |
| 7.5 Readers–Writers Problem | 146 |
| Summary | 155 |
| Notes and Further Reading | 156 |
| Exercises | 157 |
| 8 Model-Based Design | 159 |
| 8.1 From Requirements to Models | 160 |
| 8.2 From Models to Implementations | 174 |
| Summary | 178 |
| Notes and Further Reading | 180 |
| Exercises | 181 |
| 9 Dynamic Systems | 183 |
| 9.1 Golf Club Program | 184 |
| 9.2 Golf Club Model | 188 |

| | | |
|-----------|---|------------|
| 9.3 | Fair Allocation | 192 |
| 9.4 | Revised Golf Ball Allocator | 193 |
| 9.5 | Bounded Overtaking | 194 |
| 9.6 | Bounded Overtaking Golf Ball Allocator | 200 |
| 9.7 | Master–Slave Program | 202 |
| 9.8 | Master–Slave Model | 203 |
| | Summary | 205 |
| | Notes and Further Reading | 206 |
| | Exercises | 208 |
| 10 | Message Passing | 209 |
| 10.1 | Synchronous Message Passing | 209 |
| 10.2 | Asynchronous Message Passing | 219 |
| 10.3 | Rendezvous | 225 |
| | Summary | 232 |
| | Notes and Further Reading | 233 |
| | Exercises | 234 |
| 11 | Concurrent Architectures | 237 |
| 11.1 | Filter Pipeline | 237 |
| 11.2 | Supervisor–Worker | 250 |
| 11.3 | Announcer–Listener | 265 |
| | Summary | 273 |
| | Notes and Further Reading | 273 |
| | Exercises | 274 |
| 12 | Timed Systems | 277 |
| 12.1 | Modeling Timed Systems | 278 |
| 12.2 | Implementing Timed Systems | 287 |
| 12.3 | Parcel Router Problem | 295 |
| 12.4 | Space Invaders | 306 |
| | Summary | 315 |
| | Notes and Further Reading | 317 |
| | Exercises | 317 |
| 13 | Program Verification | 319 |
| 13.1 | Sequential Processes | 320 |
| 13.2 | Modeling Condition Synchronization | 322 |
| 13.3 | Modeling Variables and Synchronized Methods | 325 |
| 13.4 | Bounded Buffer Example | 328 |

| | |
|--|------------|
| 13.5 Readers–Writers Example | 334 |
| Summary | 340 |
| Notes and Further Reading | 341 |
| Exercises | 342 |
| 14 Logical Properties | 345 |
| 14.1 Fluent Propositions | 346 |
| 14.2 Temporal Propositions | 348 |
| 14.3 Fluent Linear Temporal Logic (FLTL) | 355 |
| 14.4 Database Ring Problem | 358 |
| Summary | 364 |
| Notes and Further Reading | 365 |
| Exercises | 366 |
| Appendix A FSP Quick Reference | 367 |
| A.1 Processes | 367 |
| A.2 Composite Processes | 368 |
| A.3 Common Operators | 368 |
| A.4 Properties | 369 |
| A.5 Fluent Linear Temporal Logic (FLTL) | 370 |
| Appendix B FSP Language Specification | 371 |
| B.1 FSP Description | 372 |
| B.2 Identifiers | 372 |
| B.3 Action Labels | 373 |
| B.4 const, range, set | 374 |
| B.5 Process Definition | 375 |
| B.6 Composite Process | 378 |
| B.7 Parameters | 380 |
| B.8 Re-Labeling and Hiding | 381 |
| B.9 property, progress and menu | 382 |
| B.10 Expression | 383 |
| B.11 Basic FSP | 385 |
| B.12 fluent and assert | 386 |
| Appendix C FSP Semantics | 389 |
| C.1 Labeled Transition System (LTS) | 389 |
| C.2 Processes | 390 |
| C.3 Composite Processes | 391 |
| C.4 Common Operators | 393 |

| | |
|---|------------|
| C.5 Safety Properties | 394 |
| C.6 Semantic Equivalences | 394 |
| C.7 Fluent Linear Temporal Logic (FLTL) | 395 |
| Appendix D UML Class Diagrams | 399 |
| Bibliography | 401 |
| Index | 407 |

